

DRIVE AGX Thor Development Platform

June 2025





NVIDIA DRIVE End-to-End Solutions for Autonomous Vehicles



DRIVE Thor SoC

Most advanced Automotive SoC based on Blackwell Architecture

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DRIVE AGX Thor DevKit

High-Performance Automotive Software Development Platform



DriveOS

AV Software Foundation OS, CUDA & DriveWorks



DRIVE PX 2 AutoChaffeur



2016

24 TOPS 2x Parker SoCs w/ Pascal iGPU + 2x dGPUs

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DRIVE DevKit Roadmap Leaps of performance...



2018

60 TOPS | 320 TOPS 2x Xavier SoCs w/ Volta iGPU |+2x dGPUs 2021

254 TOPS 1x Orin-X SoC w/ Ampere iGPU



2024

1000 TOPS 1x Thor-X SoC w/ Blackwell iGPU



NVIDIA DRIVE Thor-X SoC

Autonomous driving and robotics processor optimized for generative AI

Blackwell GPU with Generative AI Engine

- FP32, 16, 8, and now 4-bit floating point AI support
- Perform quantization aware training and deploy with 4-bit computation
- Up to 1000 INT8 TOPS raw deep learning performance.
- Up to 2000 FP4 TFLOPS. >20x peak Floating Point throughput vs Orin, enabling high throughput LLM inference

High Performance ARM Neoverse V3AE CPU

- High Single thread performance necessary for decision and control
- 2.3x performance SPECrate[®]2017_int_base vs Orin (est.)



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Automotive Hardware And Software Development Kit Open & scalable platform purpose-built for automotive

DRIVE AGX Thor DevKit

DriveOS – Automotive System Software Auto-grade Silicon & IO Up to 1000 INT8 TOPS | 350W



General Access target Q3'2025

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Rich IO for Development, Sensors and Vehicle Bus

- Vehicle Bus, GMSL 2 & 3, Ethernet, PCIe, USB, DisplayPort
- ISO 26262 compliant sensors supported via partners

Software Included

- <u>DriveOS</u> with DriveWorks
- Middleware, tools and algorithms
- ISO 26262 safety certifiable DriveOS QNX, drivers, and platform APIs

Safe and Performant Compute Platform

- DRIVE Thor SoC with CUDA Tensor Core GPU and Neoverse V3AE ARM64 CPU
- Architected for safety, production boards available via Tier1s







System weight: ~6.2 kg | ~14 lbs

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Spec Overview

Components		
	GPU	Inte
Thor SoC	Accelerators	Pro Opt
	CPU	ARM
Safety MC	Rer	
Storage		
Power Supply		Buil
Vehicle Wiring Harness		Add

Dor	torm	nan	\mathbf{C}

DL Inference	Up t
Memory Bandwidth	273
System RAM	64 0

Onerat	ina	Para	mei	ore
operat				

Temperature	0 to 0 to
Power TDP	350
Voltage	9V t

egrated Blackwell CUDA Tensor Core GPU

ogrammable Vision Accelerator (PVA) tical Flow Accelerator (OFA)

M Neoverse V3AE. Arm64 (v9.2-A), SMP

nesas U2A16

6 GB UFS

lt-in

ditional Accessory

to 1000 INT8 TOPS | 2000 FP4 FLOPS

GB/s

GB LPDDR5X at 3200 MHz

o 35°C (SKU10)

o 45°C (SKU12)

)W

to 16V (Static), 7V to 32V (Transient)





Bench DevKit SKU10

AC Power Supply in Chassis 110 ~ 240V input

Power cords and Vehicle Harnesse available separately for CAN, Audio, etc. bench development

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Bench and In-Vehicle DevKit



In-Vehicle DevKit SKU12

DC Power Connector

9 ~ 16V input Direct supply from Vehicle Main battery

es	Vehicle Harnesses and Mounting Plate
nent	included for in-vehicle installation

- Cables, Splitters and Adapters
- To learn more see DRIVE AGX Autonomous Vehicle Development Platform

Two DRIVE AGX Thor DevKits will be available for order

- Bench DevKit (SKU10) is for lab use with built-in power supply for AC; regional power cords are available for purchase separately
- In-Vehicle DevKit (SKU12) is for installation in the vehicle; DevKit comes with DC power connector and vehicle harnesses
- Specific Accessories are available for purchase separately (power cords, vehicle harness, 10 GbE NIC adapter)

Pictures shown are not the final product!



Auto-Grade and **Development Interfaces**

C +borpot	2x	100/1000/10G-T1	Quad H-MTD ¹	
Ethernet 3		100/1000/10G-T1	6-port H-MTD ¹	
	Зх	GMSL2 MAX96724	Quad Fakra ¹	
Camera	1x	GMSL2 MAX96712	Quad Fakra ¹	
	1x	GMSL3 MAX96792 ³	Quad Fakra ¹	
	1x	USB 3.2 (U1 for data)	USB-C	
USB	1x	USB 3.2 (U2 for flashing)	USB-C	
	1x	USB 2.0 (U3 for data)	USB-C	
	1x	USB 2.0 (Debug)	USB-C	
	2x	GMSL3 MAX96851/96861	Quad Fakra ¹	
Display 1	1x	Display out up to 4K@60Hz	DisplayPort	
PCle	2x 1x	PCIe x2 or PCIe x 4	MiniSAS HD	
Wiring Harness	2x	4x CAN, 1x FlexRay, 1x LIN, 2+1x A2B, 2x USS	Vehicle Harness Connector ^{1,2}	

¹Auto-grade connectors ²Vehicle Harness Connector not compatible with DRIVE AGX Orin DevKit ³Only 2 of 4 ports available



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Picture shown is not the final product!





Questions

Can I still purchase a DRIVE AGX Orin Devl

When can I purchase a DRIVE AGX Thor De

Where can I purchase a DRIVE AGX Thor D

What is the difference between DRIVE AGX General Access DevKits?

Will I be able to purchase a Vehicle Access DRIVE AGX Orin) for DRIVE AGX Thor SKU

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Frequently Asked Questions

	Answer
Kit?	Yes, DRIVE AGX Orin DRIVE AGX Autonom Developer for more c
evKit?	DRIVE AGX Thor Early for approved Custom Release / General Acc vehicle, will be availab
DevKit?	DRIVE AGX Thor Dev authorized Distributo Nexty).
X Thor Early Access and	Early Access DevKits Thor-X) SoC. Some I/ hardware will be upda final DevKits. P-Release DevKits wi General Access DevK certified for specific
sory Kit (that was available for 10 for bench?	No, we will not separa SKU12 for in-vehicle

DevKits will still be available for purchase. See nous Vehicle Development Platform | NVIDIA details.

ly Access DevKits, SKU10 for bench is available now ners going to production on Thor. DRIVE AGX Thor Pcess DevKits, SKU10 for bench and SKU12 for inble in Q3'2025.

/Kits will be available for purchase via NVIDIA ors (e.g., Arrow, EDOM, Leadtek, MDS, Macnica,

s are subject to change. They have early Thor-U (vs. /Os (e.g., networking card) will change. Other lated to final versions. Performance will differ from

ill use Thor-X SoC. (its will use Thor-X SoC and be commercially countries and regions.

ately sell a Vehicle Accessory Kit. DRIVE AGX Thor should be used in the vehicle.

