



# NVIDIA IGX Safety Product Brief



NVIDIA IGX, coupled with the Safety Extension Package (SEP), provides the foundations for robotics safety.

The safety support in the [NVIDIA IGX™](#) platform includes:

- > **NVIDIA Thor™ SoC Safety Extensions:** The hardware features for fault detection and control implemented in the NVIDIA IGX platform.
- > **Thor Functional Safety Island (FSI):** An independent, redundant set of processors with onboard memory implemented in the NVIDIA Thor SoC, used as a monitoring and safety processor working beside other NVIDIA Thor SoC engines. It has its own power rail and clocks to be maximally independent of other NVIDIA Thor SoC components.
- > **Safety Microcontroller Unit (SMCU):** An independent microcontroller located on the carrier board (not on the Jetson Thor SoM) that works in conjunction with the NVIDIA Thor SoC to monitor faults and trigger appropriate safety responses.

The Safety Extensions, FSI, and SMCU are used through the Safety Extension Package (SEP) software.

IGX Thor supports two software safety architectures:

- > Linux, with RTOS on FSI and sMCU
- > NV Hypervisor, supporting a Linux VM and a QNX VM on CCPLEX, with RTOS on FSI and sMCU

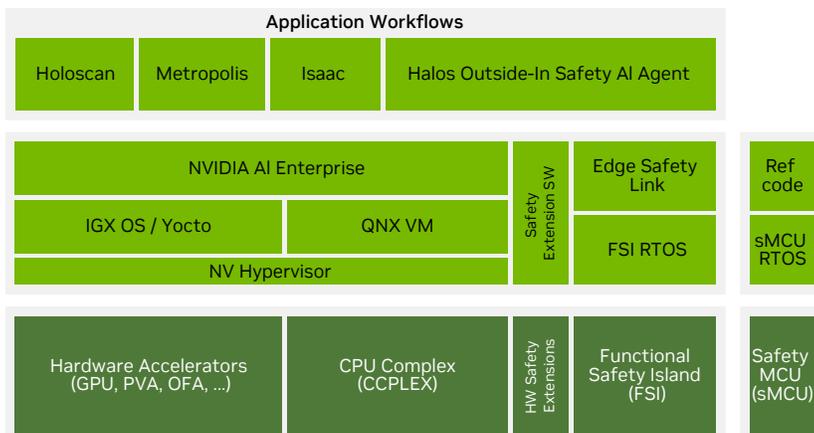
## IGX and Halos

[NVIDIA Halos](#) is a full-stack comprehensive safety system for autonomous machines unifying safety elements from vehicle and robot architecture to AI models. It comprises hardware and software elements, tools, models, and the design principles for combining them to safeguard AI-based, end-to-end AV and robotic stacks.

NVIDIA IGX is the Halos AI robotics platform to the highest degree of autonomy, while achieving robust levels of safety standards.

[NVIDIA Halos AI Systems Inspection Lab](#) (accredited by ANAB) helps to ensure that NVIDIA IGX customers meet rigorous safety and cybersecurity standards through impartial assessments.

IGX customers get an inspection report and an inspection certificate to be consumed with technical services or certification agencies for final certification.



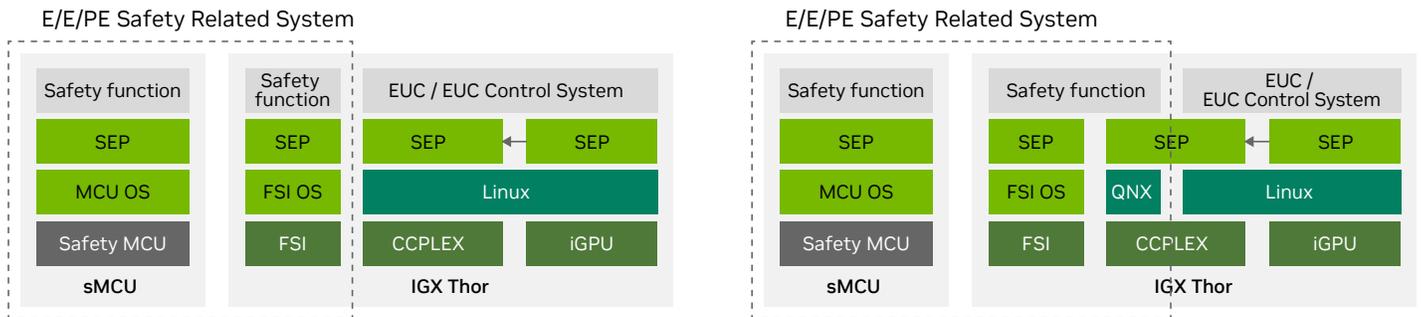
# NVIDIA IGX Functional Safety Support

SoC Safety Extensions	Safety Extension Package	Edge Safety Link	Safety Collaterals
<ul style="list-style-type: none"> <li>&gt; DRAM ECC</li> <li>&gt; Temperature, clock and voltage monitoring</li> <li>&gt; ARM RAS</li> <li>&gt; Hardware safety extensions</li> <li>&gt; IST (In System Test)</li> <li>&gt; FSI (Functional Safety Island)</li> <li>&gt; Support for Freedom From Interference (FFI)</li> </ul>	<ul style="list-style-type: none"> <li>&gt; Drivers for In System Test (IST)</li> <li>&gt; Hardware Safety Manager and Error Collator for monitoring and collating error signals</li> <li>&gt; Error Propagation to FSI and NVIDIA IGX sMCU</li> <li>&gt; Diagnostic monitoring, including Safe State and FuSa State monitoring and heartbeat mechanism at various layers</li> </ul>	<ul style="list-style-type: none"> <li>&gt; A safe and secure interface between IGX and safety-related elements, such as safety related sensors and actuators</li> <li>&gt; It is the infrastructure on which safety protocols such as FSOE, PROFIsafe, or CIP Safety can be implemented</li> </ul>	<ul style="list-style-type: none"> <li>&gt; IGX Thor in Safety-Related Systems Application Note, including how to address standards such as IEC 61508, ISO 13849, ISO 25119, ISO/IEC TS 22440, DO178C, and DO254</li> <li>&gt; Safety Manual and FMEDA</li> <li>&gt; Inspection report by TUV Rheinland</li> </ul>

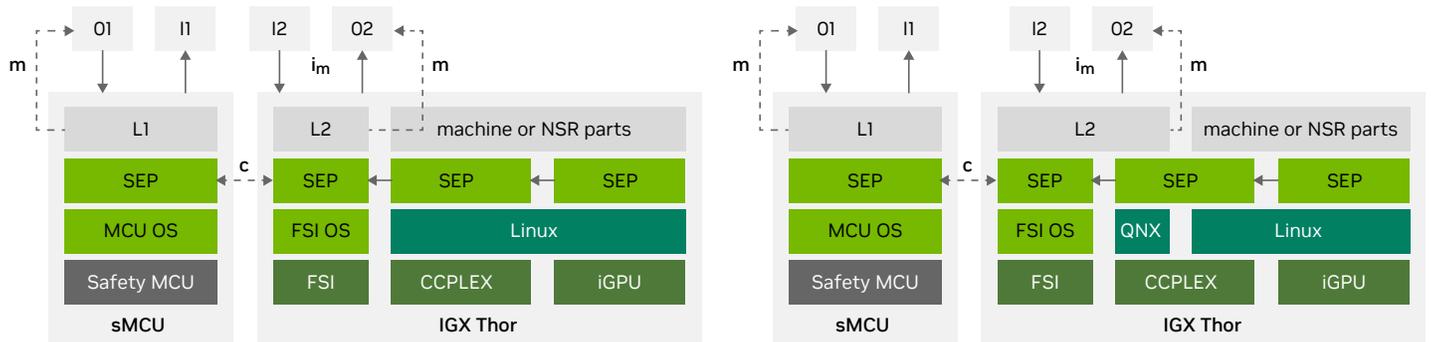
## Safety Architecture Examples

Additional examples available in IGX Thor in the Safety-Related Systems Application Note.

### IEC 61508 Architecture Examples



### ISO 13849 Architecture Examples



Customer SW   3rd party HW   IGX SW   SEP

## NVIDIA IGX Functional Safety Support Summary

- > **Avoidance of systematic failures in hardware.** The process with which IGX SoC has been developed is compliant with ISO 26262 up to ASIL D and compatible up to IEC 61508 SIL 3 / SC 3.
- > **Avoidance of systematic failures in software.** The process with which IGX SEP has been developed (named “NVIDIA PLC-L3”) is compliant with ISO 26262 up to ASIL D and compatible up to IEC 61508 SIL 3 / SC 3.
- > **Detection and control of random hardware failures.** Thor SoC meets the applicable ISO 26262 requirements for random hardware integrity of ASIL B and compatible up to IEC 61508 SIL 2 (SIL 3 for Safety Island).

## Ready to Get Started?

[Learn more](#) about NVIDIA Halos.

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