



BACKGROUND

MFCC

The mel-frequency cepstrum (MFC) is a representation of the short-term power spectrum of a sound, based on a linear cosine transform of a log power spectrum on a nonlinear mel-scale of frequency.

MFCCs are coefficients that collectively make up an MFC

GPU Computing

Multicore: yoke of oxen

Each core optimized for executing a single thread

Manycore: flock of chickens

Cores optimized for aggregate throughput, deemphasizing individual performance





CUDA & OpenCL

CUDA is a recent programming model, designed for

Manycore architectures

Wide SIMD parallelism

Scalability

CUDA provides:

A thread abstraction to deal with SIMD

Synchronization & data sharing between small groups of threads

CUDA programs are written in C + extensions

OpenCL is the open standard for parallel programming of heterogeneous systems

HW Architecture of GPU



Kernel is SIMD – with multiple threads

GPU Based Feature Extraction Implementation Haofeng Kou, Weijia Shang, Jike Chong, Ian Lane Santa Clara University & Carnegie Mellon University





RESULTS

Num. Frames	CPU Program (msec)	GPU Program (msec)	Ratio (Tcpu / Tgpu)
1	0.57	<0.1	>5.7
5	0.81	< 0.1	>8.1
10	1.1	< 0.1	>11
20	1.69	0.1	16.9
50	3.46	0.2	17.3
100	6.41	0.3	21.4
250	15.26	0.6	25.4
500	28.11	1.2	23.4
1000	53.85	2.3	23.4
CPU:	processor : 4 model name : Intel(R) Core(TM)2 Quad CPU Q8300 @ 2.50GHz cpu MHz : 2003.000 cache size : 2048 KB		
GPU:	GeForce GTX 460 CUDA @ SCU CUDA Driver Version / Runtime Version 4.0 / 4.0 CUDA Capability Major/Minor version number: 2.1		
OS:	2.6.31-14-generic #48-Ubuntu SMP Fri Oct 16 14:05:01 UTC 2009 x86_64 GNU/Linux ubuntu 9.10 karmic gcc version 4.4.1 (ubuntu 4.4.1-4ubuntu8)		
	12/09/2011		

CONCLUSIONS

GPU based Feature Extraction shows up to about 25 times faster than CPU version for large amount of frame data.

The similar implementation could be used for other signal processing software to improve performance.

FUTURE WORKS

Porting the x86/GPU based Feature Extraction to the SOC platform which means most likely the code will be ported to the ARM SOC with GPU build-in, for example the Tegra with GPU from Nvidia or the TI OMAP5 with GPU and so on.

Besides CUDA, OpenCL and OpenGL are also on the list of evaluation.

